

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
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Ga Won LEE)	
)	
Application No.: Unassigned)	Group Art Unit: Unassigned
(Divisional of Application No. 10/028,972)	
filed on December 28, 2001)	
)	
Filed: March 29, 2004)	Examiner: Unassigned
)	
For: METHOD OF FORMING)	
INTER-DIELECTRIC LAYER IN)	
SEMICONDUCTOR DEVICE)	

Commissioner for Patents
U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window, Mail Stop Patent Application
Crystal Plaza Two, Lobby, Room 1B03
Arlington, VA 22202

Sir:

PRELIMINARY AMENDMENT

Prior to examination of the above-referenced divisional application, please amend the application as follows: